Proposal of thesis topic for mgr inż. (MSE) programme
in Telecommunications and Computer Science

1. Topic: Hardware implementation of SPIHT image coding algorithm in FPGA device

2. Supervisor: Piotr Wasilewski

3. Auxiliary supervisor:

4. Goals and scope of the work:

An objective of the dissertation is to design hardware architecture suitable for implementation if FPGA (Field Programmable Gate Array) device SPIHT image coding algorithm.

The ability for the hardware implementation of the SPIHT should be investigated and some modifications (if necessary) should be introduced into the original SPIHT algorithm.

5. Prerequisites (e.g. experience in writing programs in a computer language or knowledge of a foreign language):

Knowledge of digital logic, programmable logic devices, ability for reading technical papers, application notes and data sheets of integrated circuits, C++ basic experience

6. Literature:

Materials will be provided by the supervisor

Łódź, 2004-04-06

Supervisor’s signature

THEESIS TOPIC SELECTION ACKNOWLEDGMENT

Student’s name: _____________________________ Student’s ID _________

Faculty: _____________________________

Main subject/major/module: _____________________________

Date and student’s signature

Supervisor’s signature

90-924 Łódź, ul. Wólczańska 223, tel. (+48 42) 636 00 65, 631 26 26, fax (+48 42) 636 22 38
http://www.eletel.p.lodz.pl